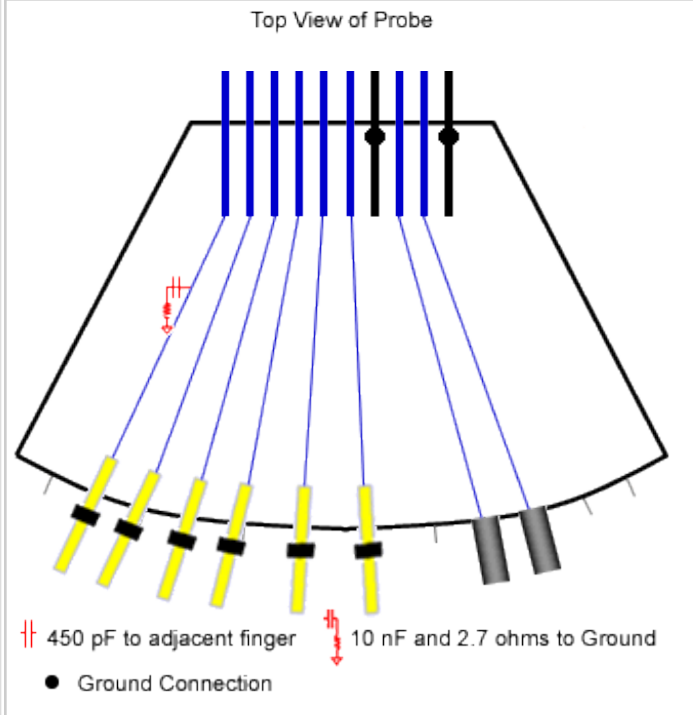


DESIGN CAPTURE: Unity Probe™ (15803)

Probe Preview

UNITY-10-02-100W

Design Name: ITERA_Top



Config Code: PLLLLLGSSG

Pin #	Contact	Type	Code
1		No Contact	
2	1	Power	P
3	2	Logic/Signal	L
4	3	Logic/Signal	L
5	4	Logic/Signal	L
6	5	Logic/Signal	L
7	6	Logic/Signal	L
8	7	Ground	G
9	8	RF Signal (GS)	S
10	9	RF Signal (SG)	S
11	10	Ground	G
12		No Contact	

Special Instructions

Customer Information

Company	University of California, Berkeley
Address	2108 Allston Way #202 Berkeley, CALIFORNIA US 94704
Requestor	Ethan Chou - echou35@berkeley.edu - 4088568493
Buyer	--
Tech	--

Notes

FFI Only Information

Submitted Date:	08/24/2023	QAD:	UNITY-10-02
Due Date:		PCB:	146-591
Work Order:		Tip:	120-588
PO Number:		Cables:	147-295 (6) 147-364 (2)